What is claimed is:

1. An apparatus for providing a crypto key and an associated checkword of said crypto key to an encryption device for a telemeter system of a missile, said apparatus comprising:

loading means for generating said crypto key and said associated checkword;

control means connected to said loading means to receive said crypto key and said associated checkword from said loading means, said control means sending a first logic signal to said loading means to effect a transfer of said crypto key and said associated checkword from said loading means to said control means for storage within said control means;

said control means being connected to said encryption device, said control means sending a second logic signal to said encryption device to initiate a load of said crypto key and said associated checkword into said encryption device;

said control means receiving from said encryption device a third logic signal, said control means, responsive to said third logic signal, loading said crypto key and said associated checkword into

said encryption device;

said control means being connected to a transmitter for the telemeter system of said missile, said control means providing a fourth logic signal to said transmitter to disable said transmitter when said crypto key and said associated checkword are loaded into said encryption device preventing said crypto key and said associated checkword from being transmitted by said transmitter; and

said control means being connected to a missile interface within said missile to receive a fifth logic signal from said missile interface upon a launch of said missile, said control means, responsive to said fifth logic signal, erasing said crypto key and said associated checkword from said control means.

2. The apparatus of claim 1 wherein said control means comprises an 8-bit Microcontroller.

3. The apparatus of claim 1 wherein said control means includes an EEPROM for storing said crypto key and said associated checkword and a copy of said crypto key and said associated checkword.

4. The apparatus of claim 1 further comprising a light emitting diode connected to said control means, said light emitting diode displaying a status for a load of said crypto key and said associated checkword into said encryption device.

5. The apparatus of claim 1 further comprising a light emitting diode connected to said control means, said light emitting diode displaying a status for an erase of said crypto key and said associated checkword from said microcontroller.

6. An apparatus for providing a crypto key and an associated checkword of said crypto key to an encryption device for a telemeter system of a missile, said apparatus comprising:

a key loader having said crypto key and said associated checkword stored therein;

a microcontroller connected to said key loader to receive said crypto key and said associated checkword from said key loader, said microcontroller sending a first variable request signal to said key loader to effect a transfer of

said crypto key and said associated checkword from said key loader to said microcontroller for storage within said microcontroller;

said microcontroller being connected to said encryption device, said microcontroller sending a sense in signal to said encryption device to initiate a load of said crypto key and said associated checkword into said encryption device;

said microcontroller receiving from said encryption device a second variable request signal, said microcontroller, responsive to said second variable request, loading said crypto key and said associated checkword into said encryption device; and

said microcontroller being connected to a transmitter for the telemeter system of said missile, said microcontroller providing a transmitter disable signal to said transmitter to disable said transmitter when said crypto key and said associated checkword are loaded into said encryption device preventing said crypto key and said associated checkword from being transmitted by said transmitter.

7. The apparatus of claim 6 wherein said microcontroller comprises an 8-bit Microcontroller.

8. The apparatus of claim 6 wherein said microcontroller includes an EEPROM for storing said crypto key and said associated checkword and a copy of said crypto key and said associated checkword.

9. The apparatus of claim 6 further comprising a light emitting diode connected to said microcontroller, said light emitting diode displaying a status for a load of said crypto key and said associated checkword into said encryption device.

10. The apparatus of claim 6 wherein said microcontroller is connected to a missile interface within said missile to receive a launch signal from said missile interface upon a launch of said missile, said microcontroller, responsive to said launch signal, erasing said crypto key and said associated checkword from said microcontroller.

11. The apparatus of claim 10 further comprising a light emitting diode connected to said microcontroller, said

light emitting diode displaying a status for an erase of said crypto key and said associated checkword from said microcontroller.

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12. The apparatus of claim 6 wherein said microcontroller is connected to a loader interface within said missile to receive an erase signal from said loader interface, said microcontroller, responsive to said erase signal, erasing said crypto key and said associated checkword from said microcontroller.

13. An apparatus for providing a crypto key and an associated checkword of said crypto key to an encryption device for a telemeter system of a missile, said apparatus comprising:

a key loader having said crypto key and said associated checkword stored therein;

a microcontroller connected to said key loader to receive said crypto key and said associated checkword from said key loader, said microcontroller sending a first variable request signal to said key loader to effect a transfer of said crypto key and said associated checkword from said key loader to said microcontroller for

storage within said microcontroller;

said microcontroller being connected to said encryption device, said microcontroller sending a sense in signal to said encryption device to initiate a load of said crypto key and said associated checkword into said encryption device;

said microcontroller receiving from said encryption device a second variable request signal, said microcontroller, responsive to said second variable request, loading said crypto key and said associated checkword into said encryption device;

said microcontroller being connected to a transmitter for the telemeter system of said missile, said microcontroller providing a transmitter disable signal to said transmitter to disable said transmitter when said crypto key and said associated checkword are loaded into said encryption device preventing said crypto key and said associated checkword from being transmitted by said transmitter;

a first light emitting diode connected to said microcontroller, said first light emitting diode displaying a status for a load of said crypto key and said associated checkword into said encryption

device;

said microcontroller being connected to a missile interface within said missile to receive a launch signal from said missile interface upon a launch of said missile, said microcontroller, responsive to said launch signal, erasing said crypto key and said associated checkword from said microcontroller;

a second light emitting diode connected to said microcontroller, said second light emitting diode displaying a status for an erase of said crypto key and said associated checkword from said microcontroller.

14. The apparatus of claim 13 wherein said microcontroller comprises an 8-bit Microcontroller.

15. The apparatus of claim 13 wherein said microcontroller includes an EEPROM for storing said crypto key and said associated checkword and a copy of said crypto key and said associated checkword.

16. The apparatus of claim 13 wherein said microcontroller is connected to a loader interface within

said missile to receive an erase signal from said loader interface, said microcontroller, responsive to said erase signal, erasing said crypto key and said associated checkword from said microcontroller.